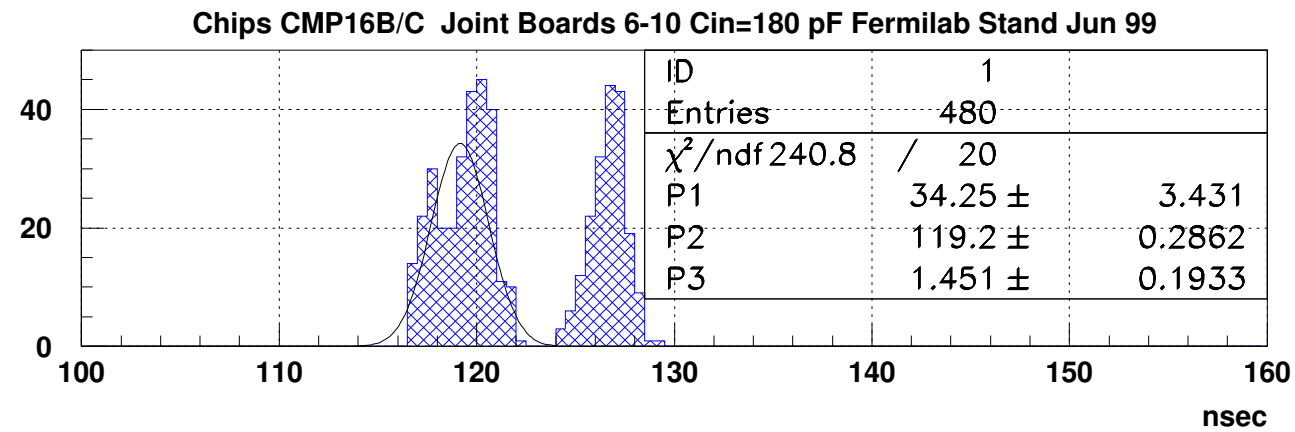
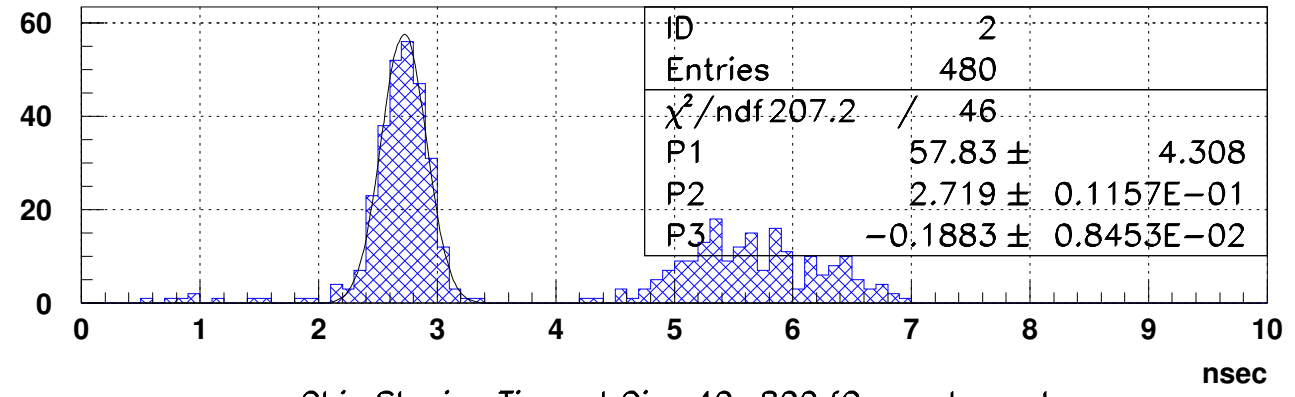
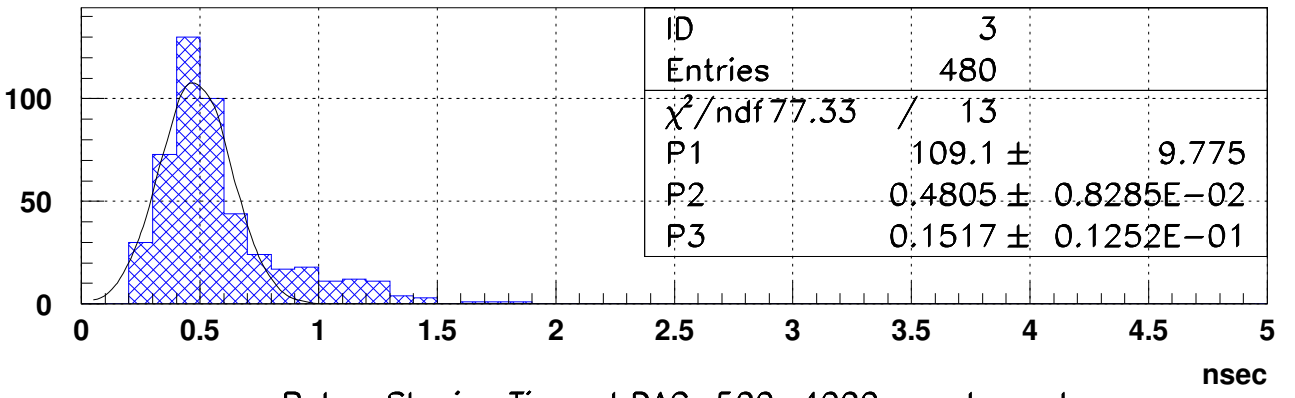


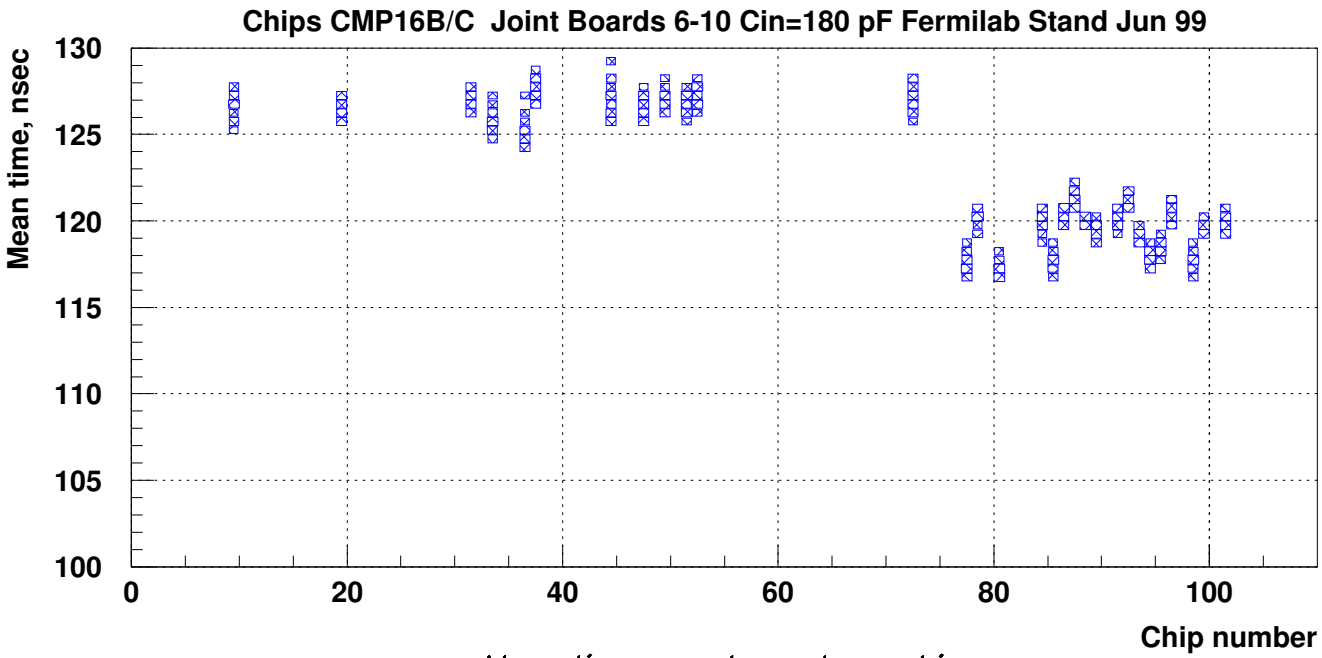
Mean time at Qin=100 fC per channel



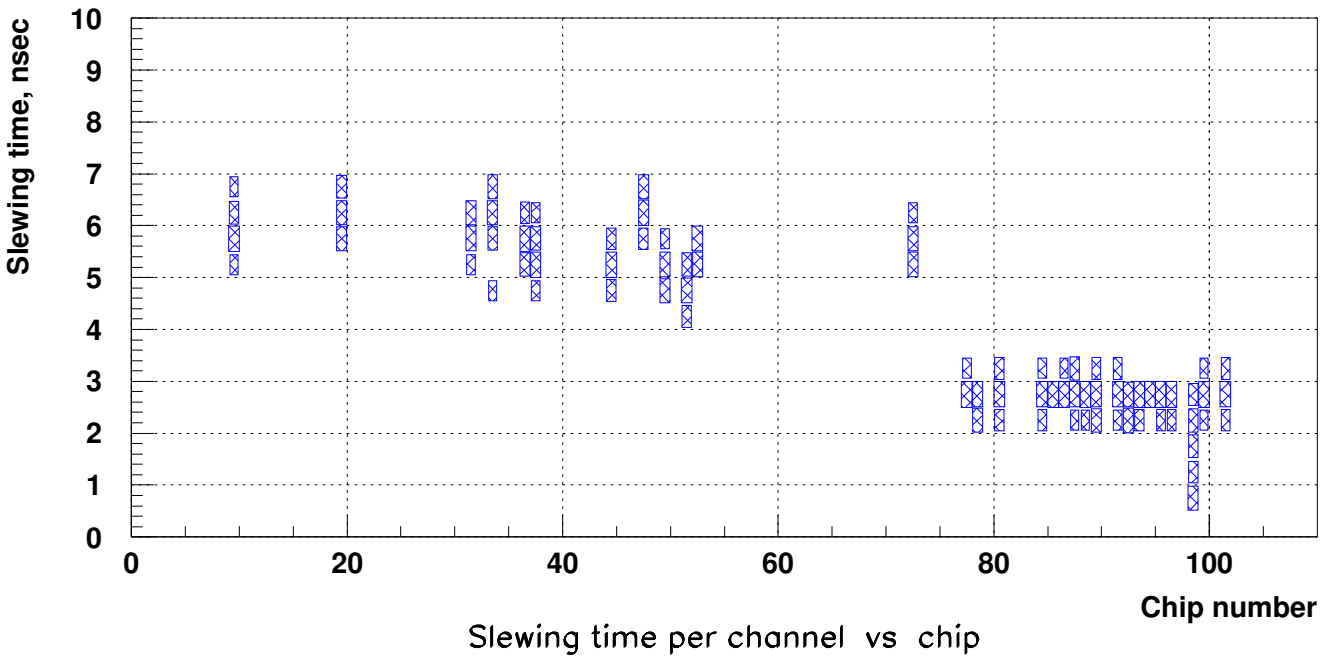
Chip Slewing Time at Qin=40−800 fC per channel



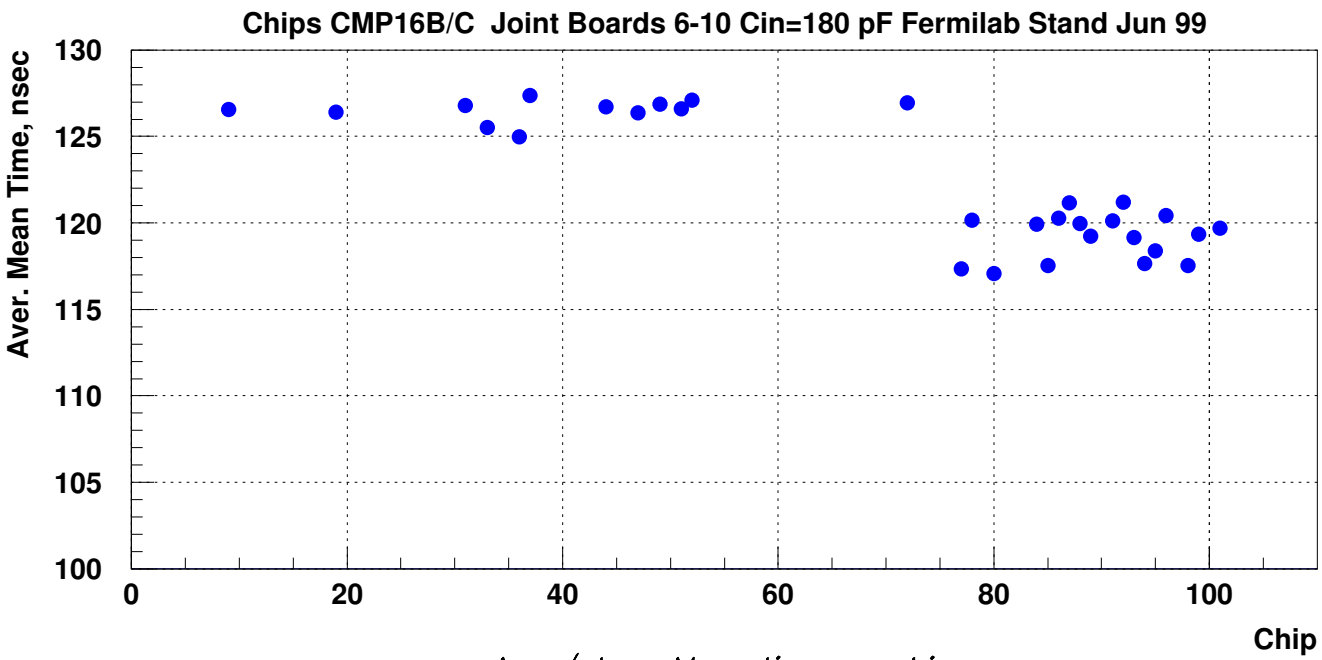
Pulser Slewing Time at DAC=500−4000 per channel



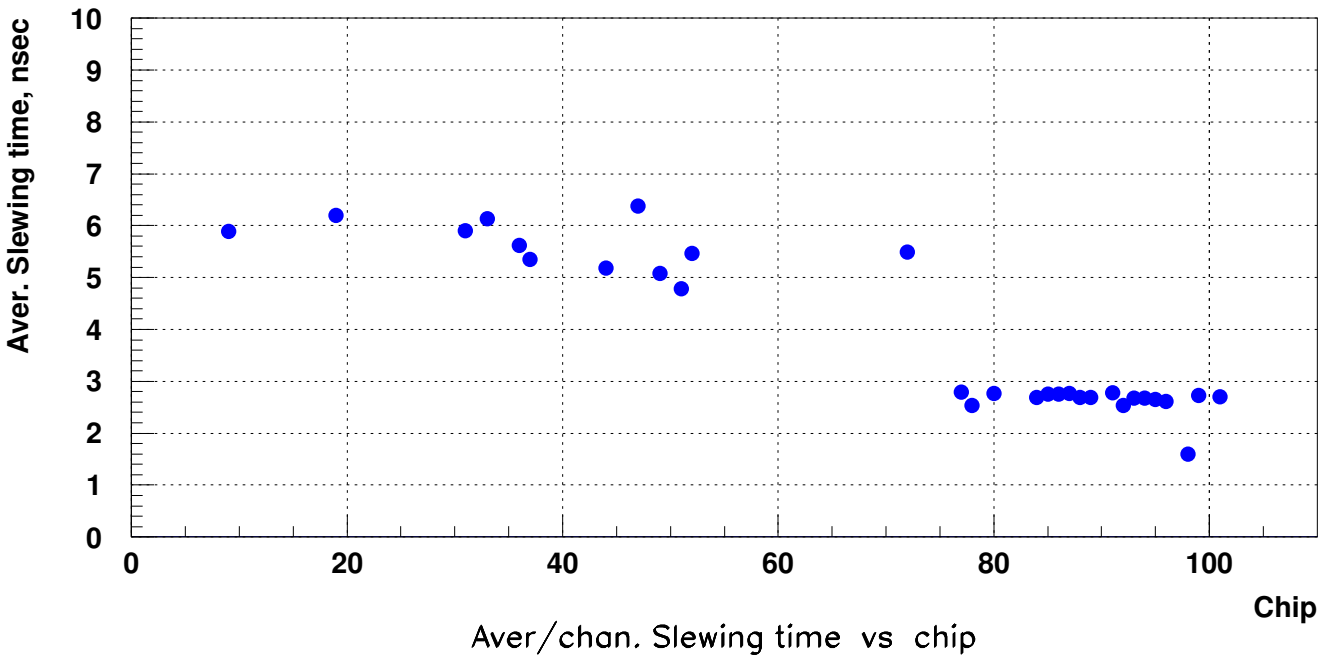
Mean time per channel vs chip



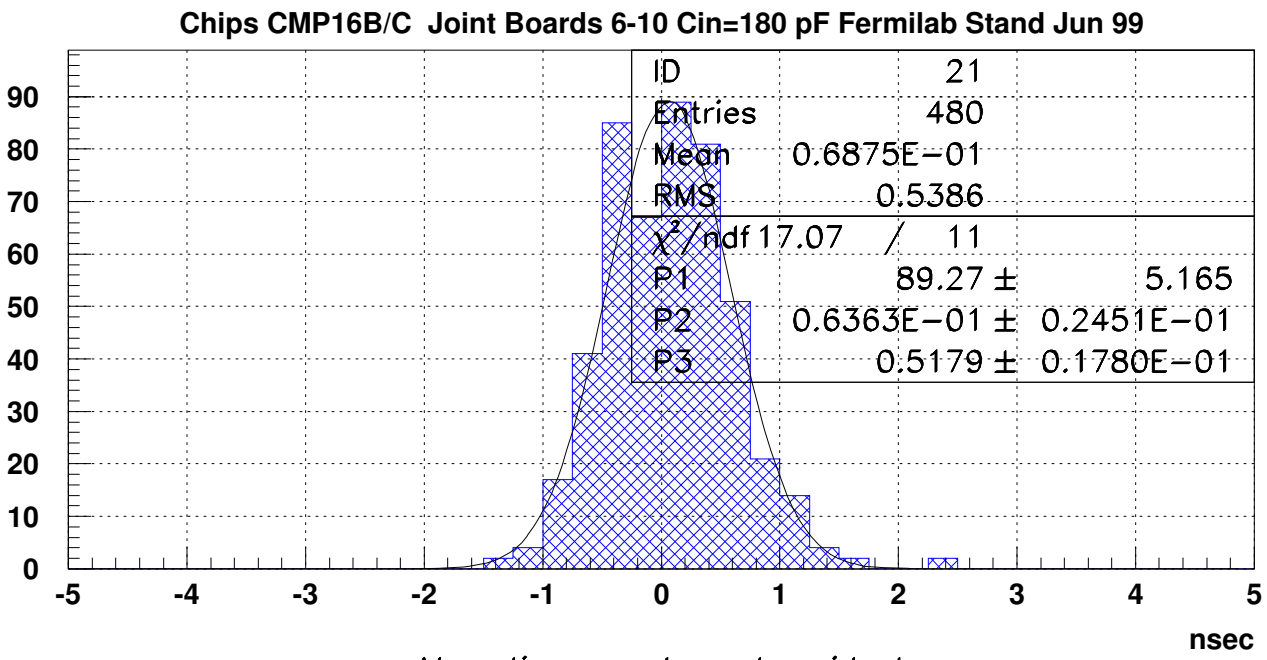
Slewing time per channel vs chip



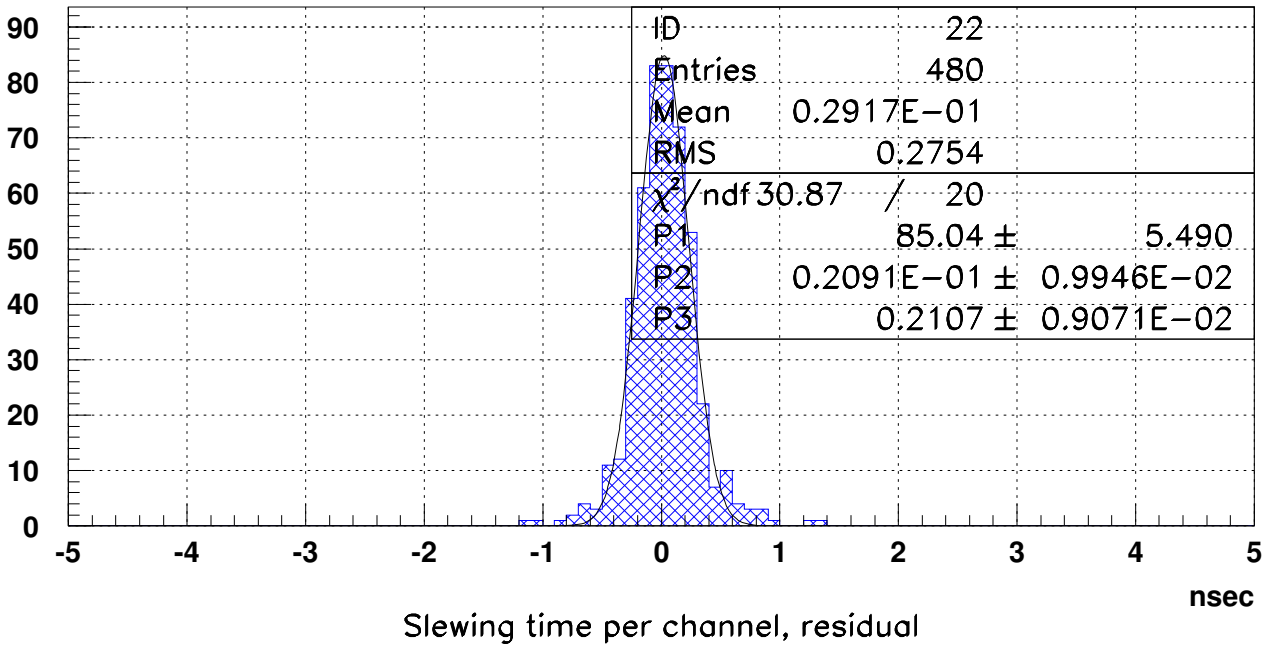
Aver/chan. Mean time vs chip



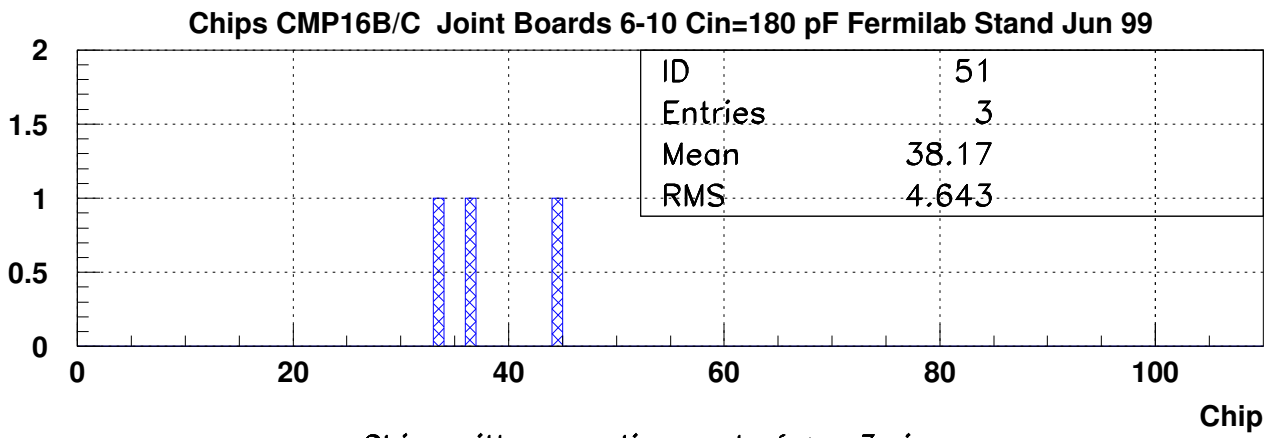
Aver/chan. Slewing time vs chip



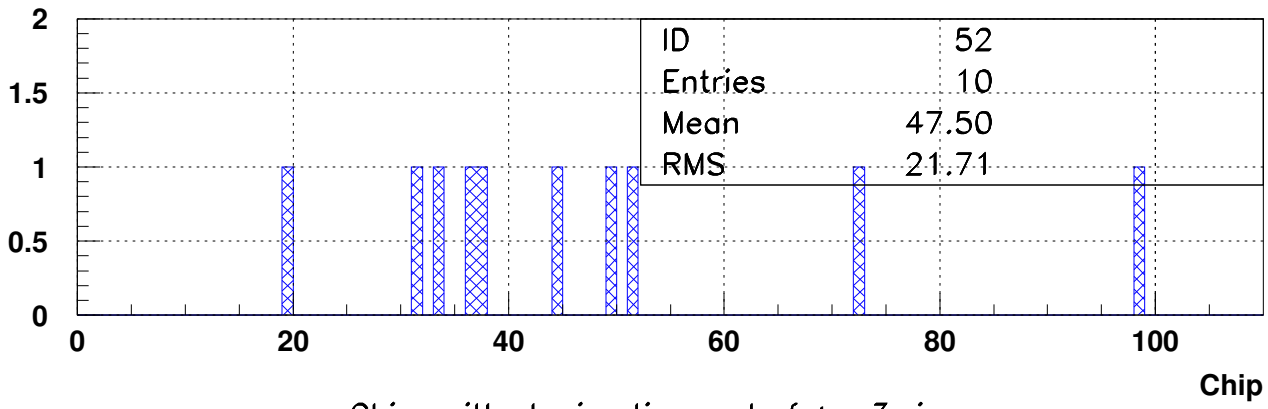
Mean time per channel, residual



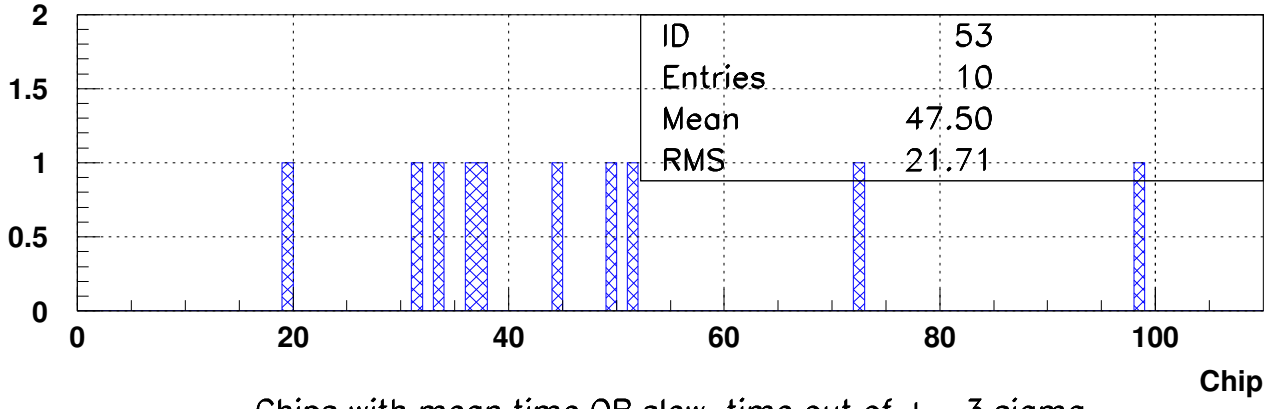
Slewing time per channel, residual



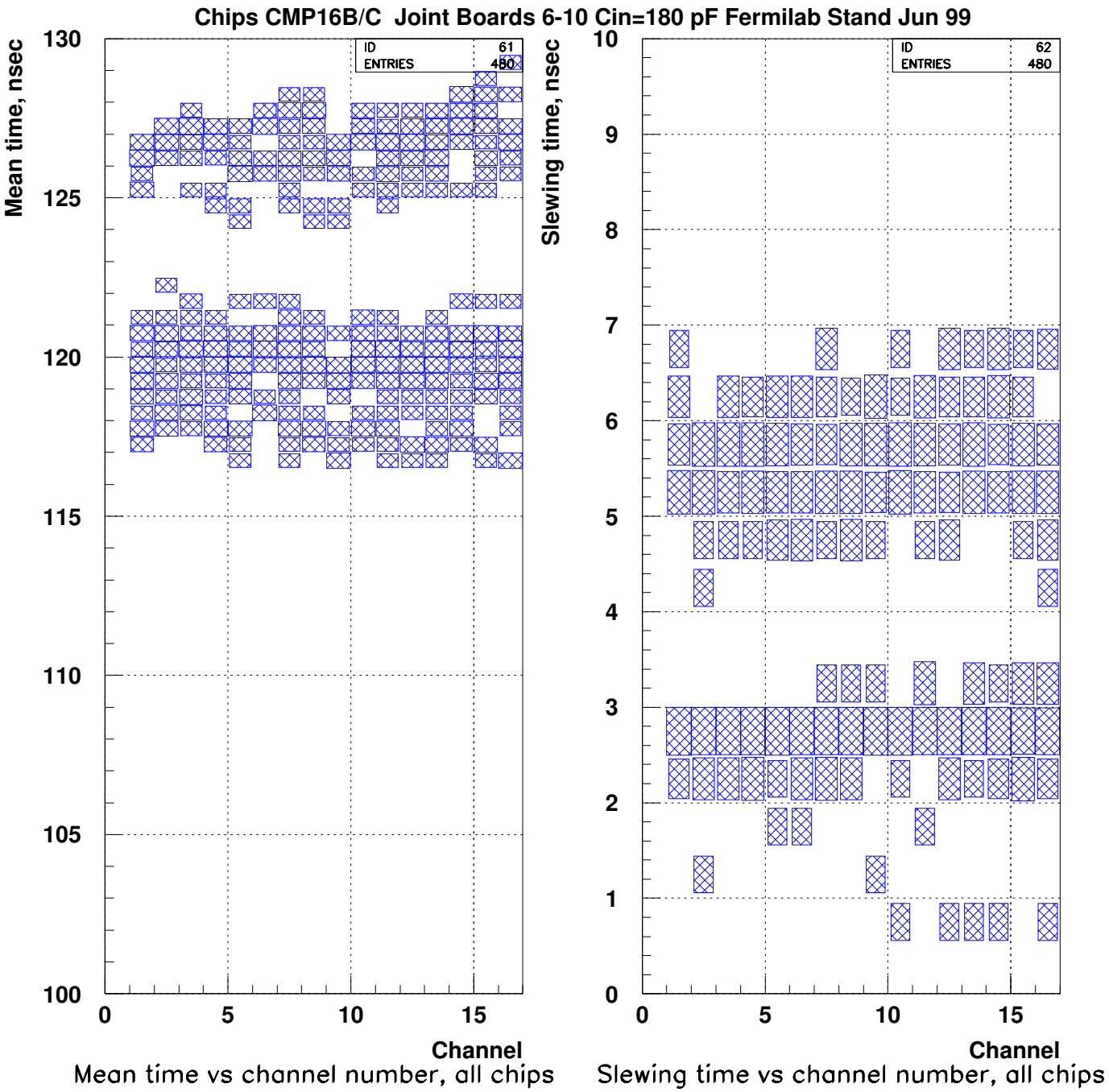
Chips with mean time out of +− 3 sigma



Chips with slewing time out of +− 3 sigma

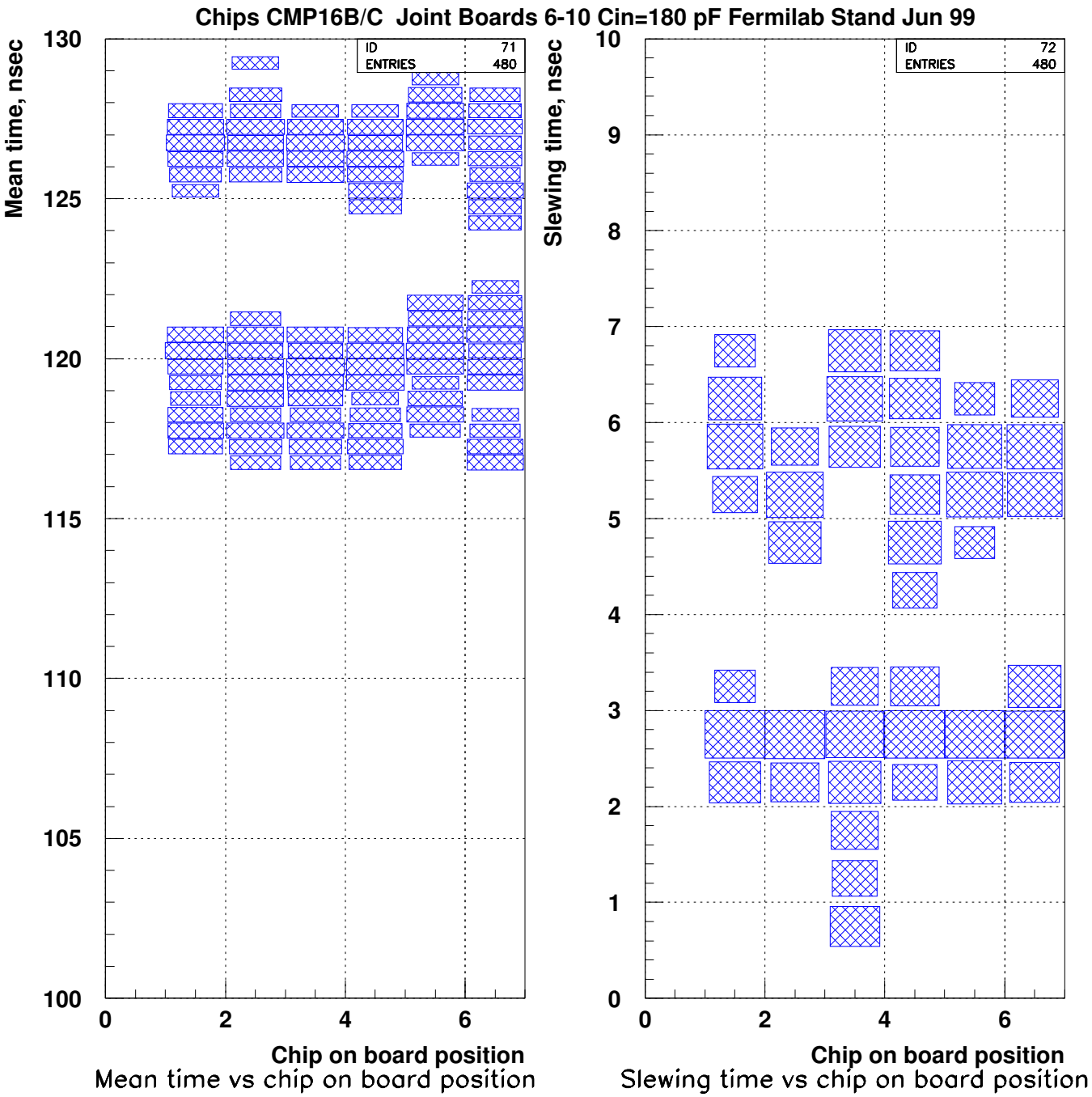


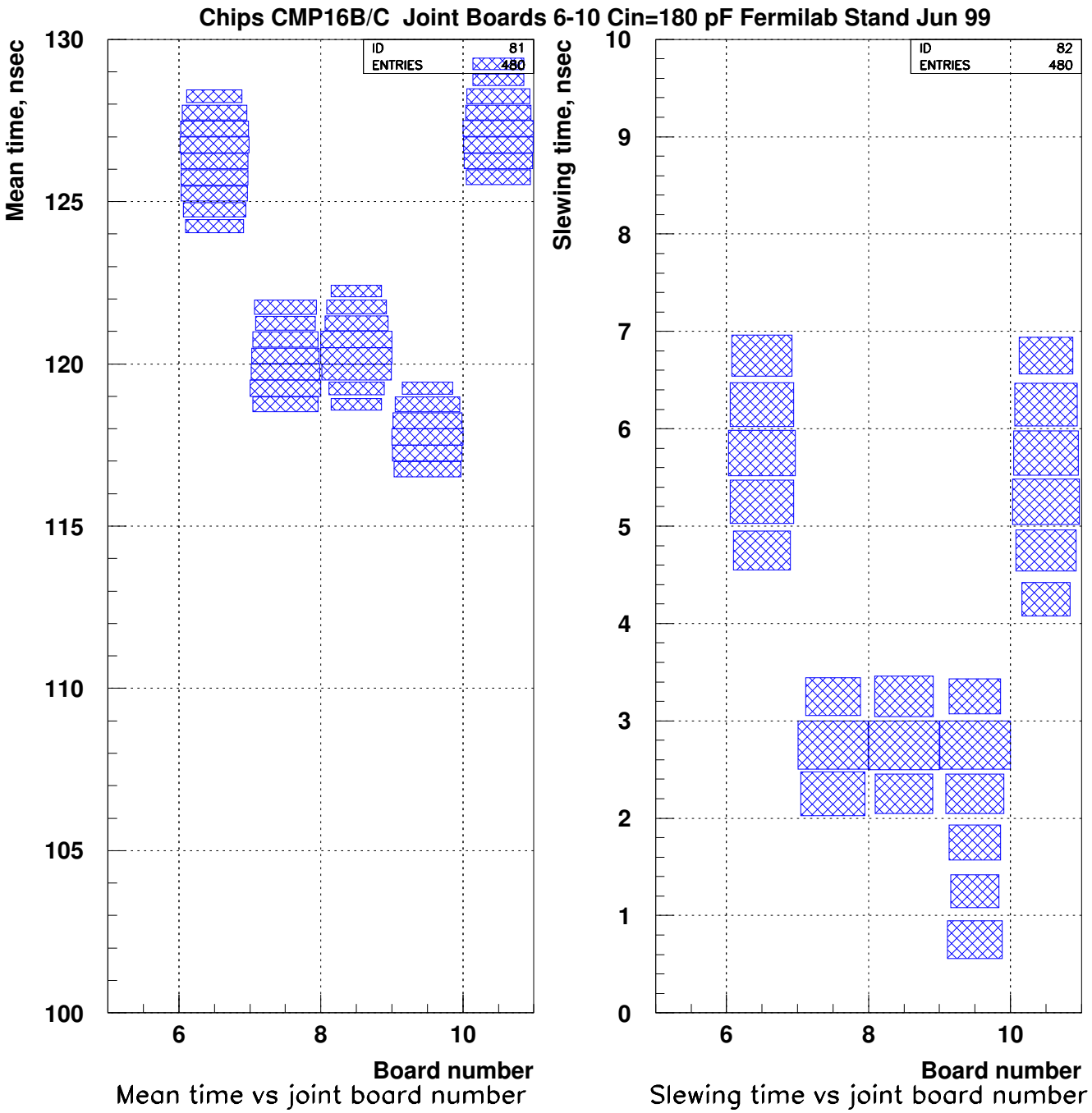
Chips with mean time OR slew. time out of +− 3 sigma

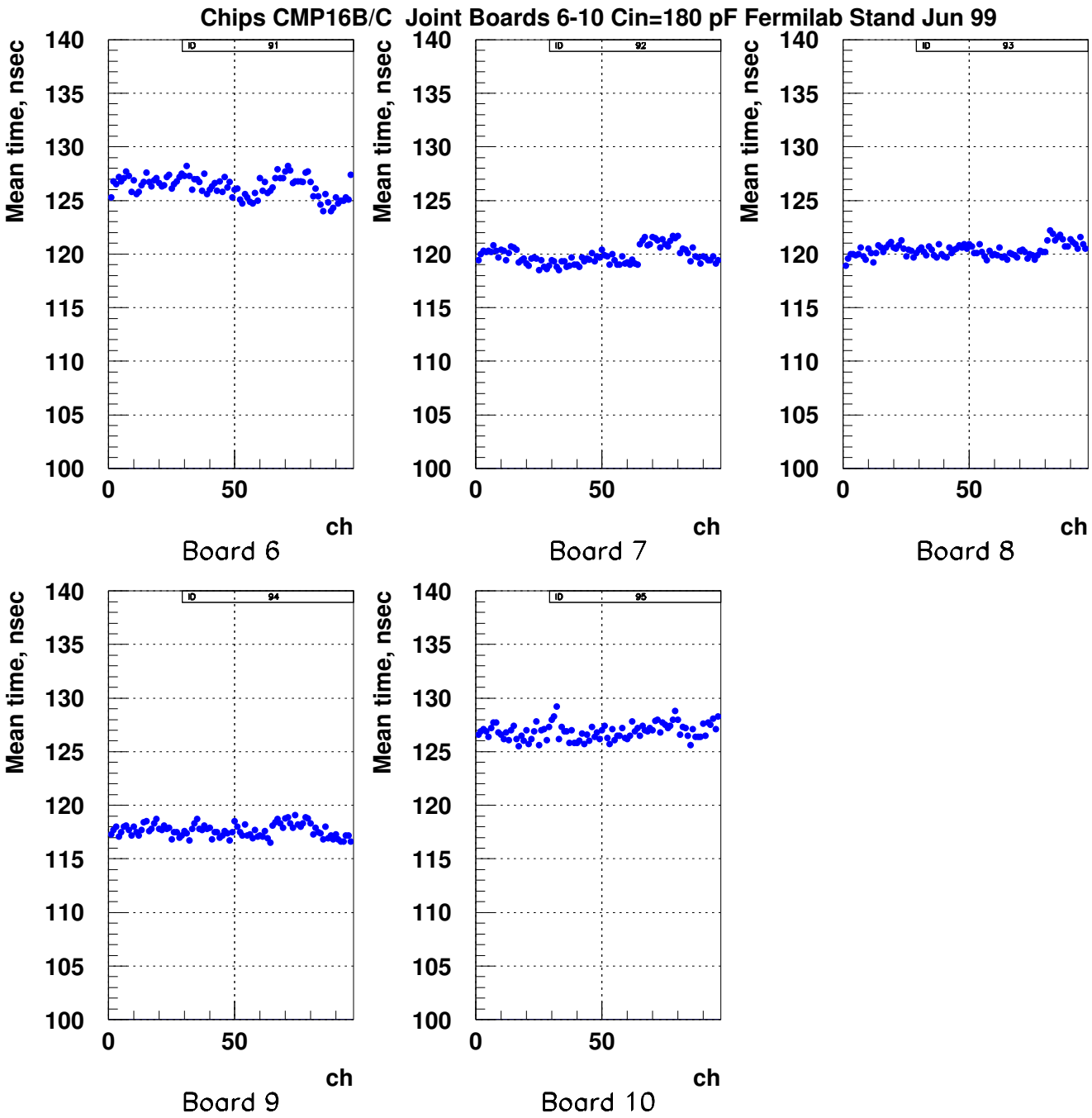


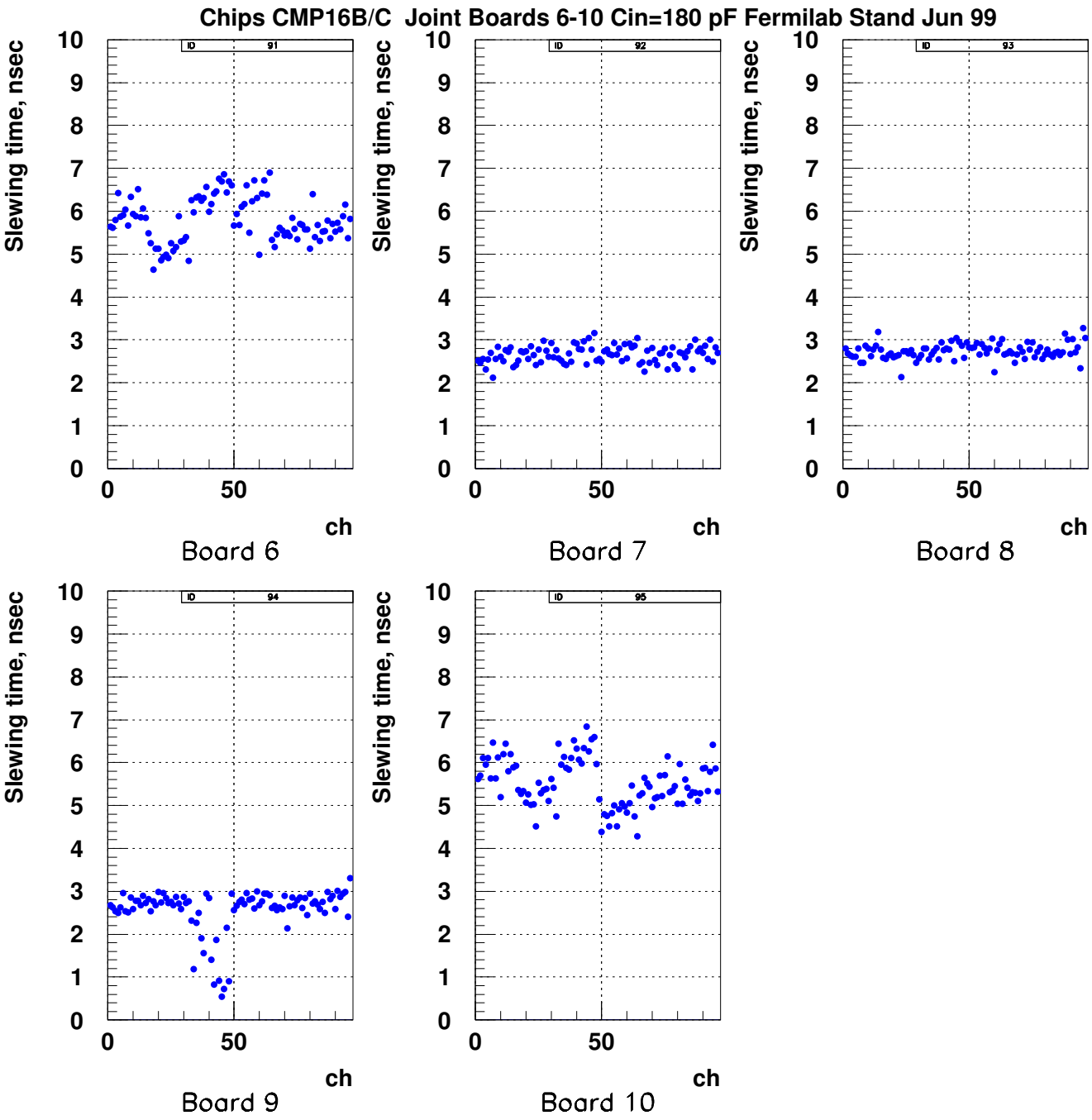
Mean time vs channel number, all chips

Slewing time vs channel number, all chips


Chips CMP16B/C Joint Boards 6-10 Cin=180 pF Fermilab Stand Jun 99
ID 71
ENTRIES 480
Mean time, nsec
130
125
120
115
110
105
100
0
2
4
6
Chip on board position
Mean time vs chip on board position
ID 72
ENTRIES 480
Slewing time, nsec
10
9
8
7
6
5
4
3
2
1
0
0
2
4
6
Chip on board position
Slewing time vs chip on board position


Chips CMP16B/C Joint Boards 6-10 Cin=180 pF Fermilab Stand Jun 99
Mean time, nsec
130
125
120
115
110
105
100
ID 81
ENTRIES 480
6
8
10
Board number
Mean time vs joint board number
Slewing time, nsec
10
9
8
7
6
5
4
3
2
1
0
ID 82
ENTRIES 480
6
8
10
Board number
Slewing time vs joint board number


Chips CMP16B/C Joint Boards 6-10 Cin=180 pF Fermilab Stand Jun 99
Mean time, nsec
140
135
130
125
120
115
110
105
100
0
50
ch
ID 91
Board 6
ID 92
Board 7
ID 93
Board 8
ID 94
Board 9
ID 95
Board 10


Chips CMP16B/C Joint Boards 6-10 Cin=180 pF Fermilab Stand Jun 99
Slewing time, nsec
ch
ID 91
Board 6
ID 92
Board 7
ID 93
Board 8
ID 94
Board 9
ID 95
Board 10