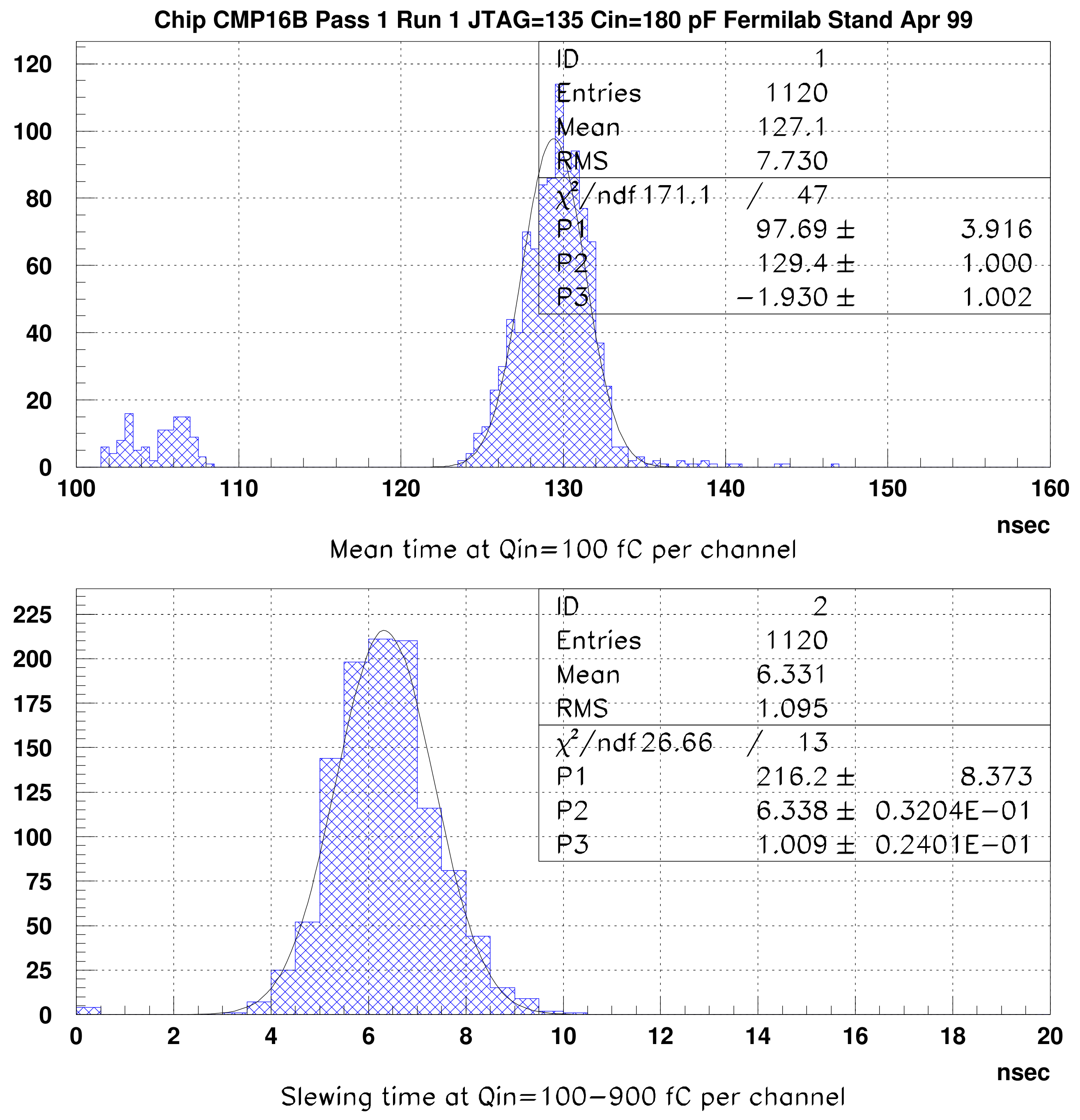
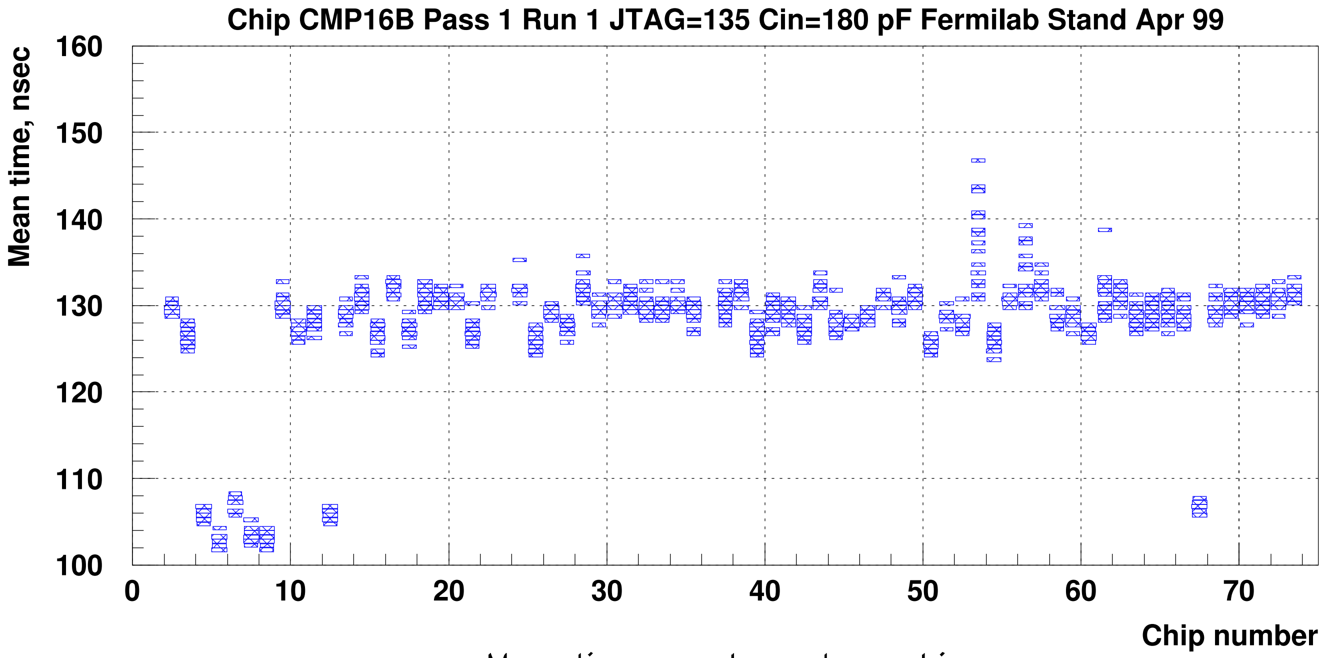
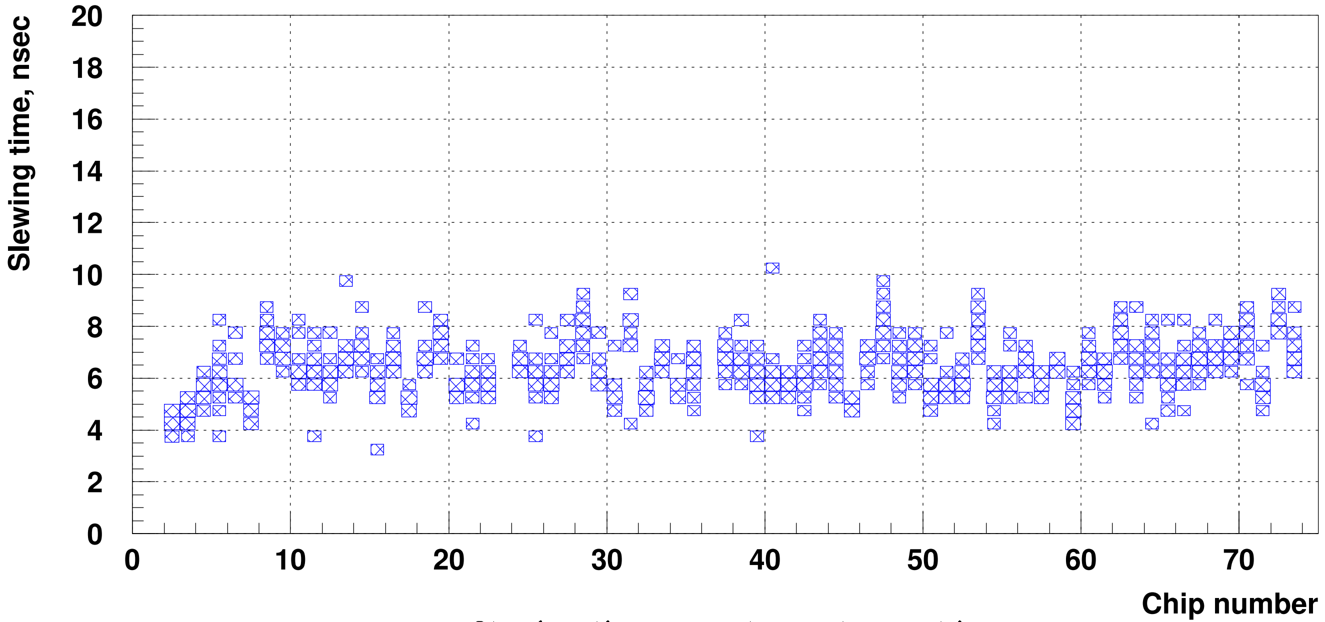
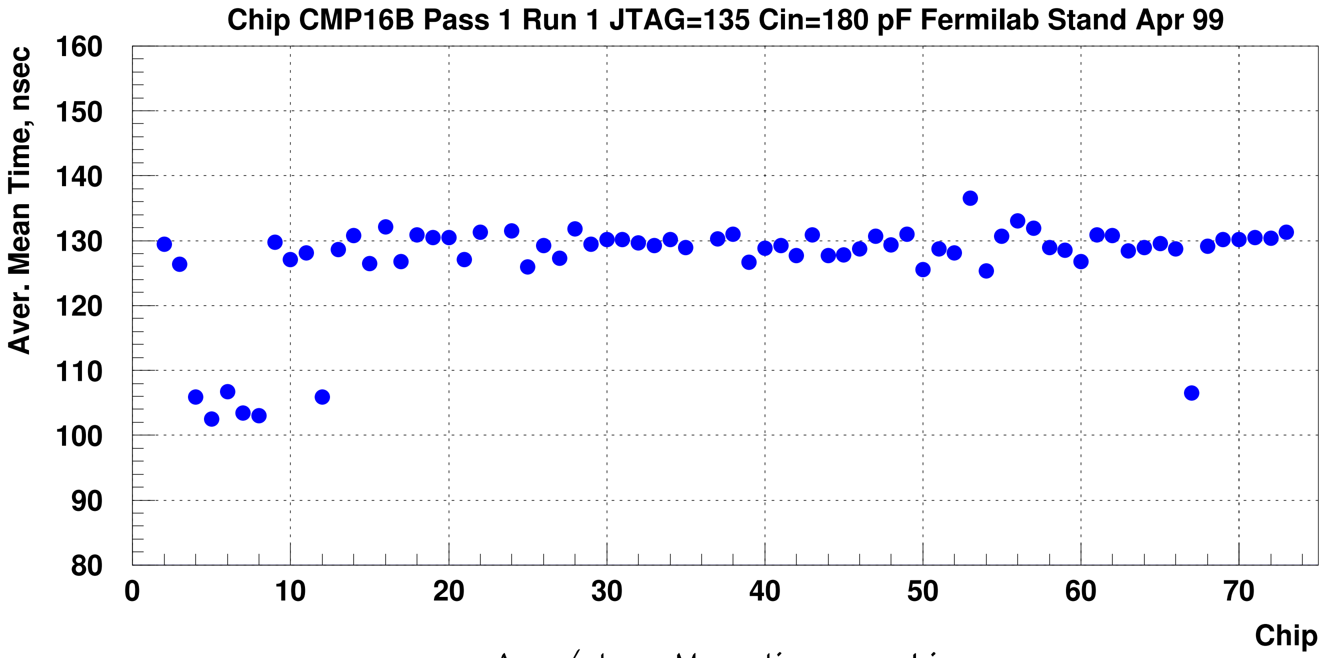

Chip CMP16B Pass 1 Run 1 JTAG=135 Cin=180 pF Fermilab Stand Apr 99
ID 1
Entries 1120
Mean 127.1
RMS 7.730
χ²/ndf 171.1 / 47
P1 97.69 ± 3.916
P2 129.4 ± 1.000
P3 −1.930 ± 1.002
nsec
Mean time at Qin=100 fC per channel
ID 2
Entries 1120
Mean 6.331
RMS 1.095
χ²/ndf 26.66 / 13
P1 216.2 ± 8.373
P2 6.338 ± 0.3204E−01
P3 1.009 ± 0.2401E−01
nsec
Slewing time at Qin=100−900 fC per channel



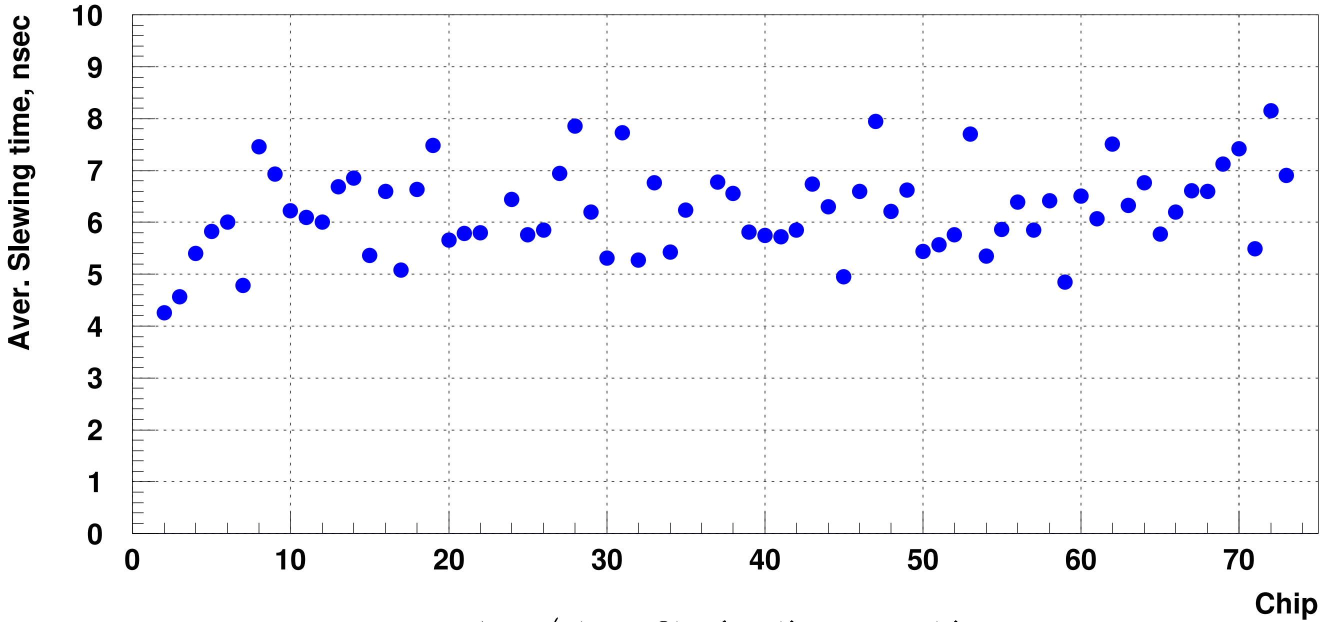
Mean time per channel vs chip



Slewing time per channel vs chip

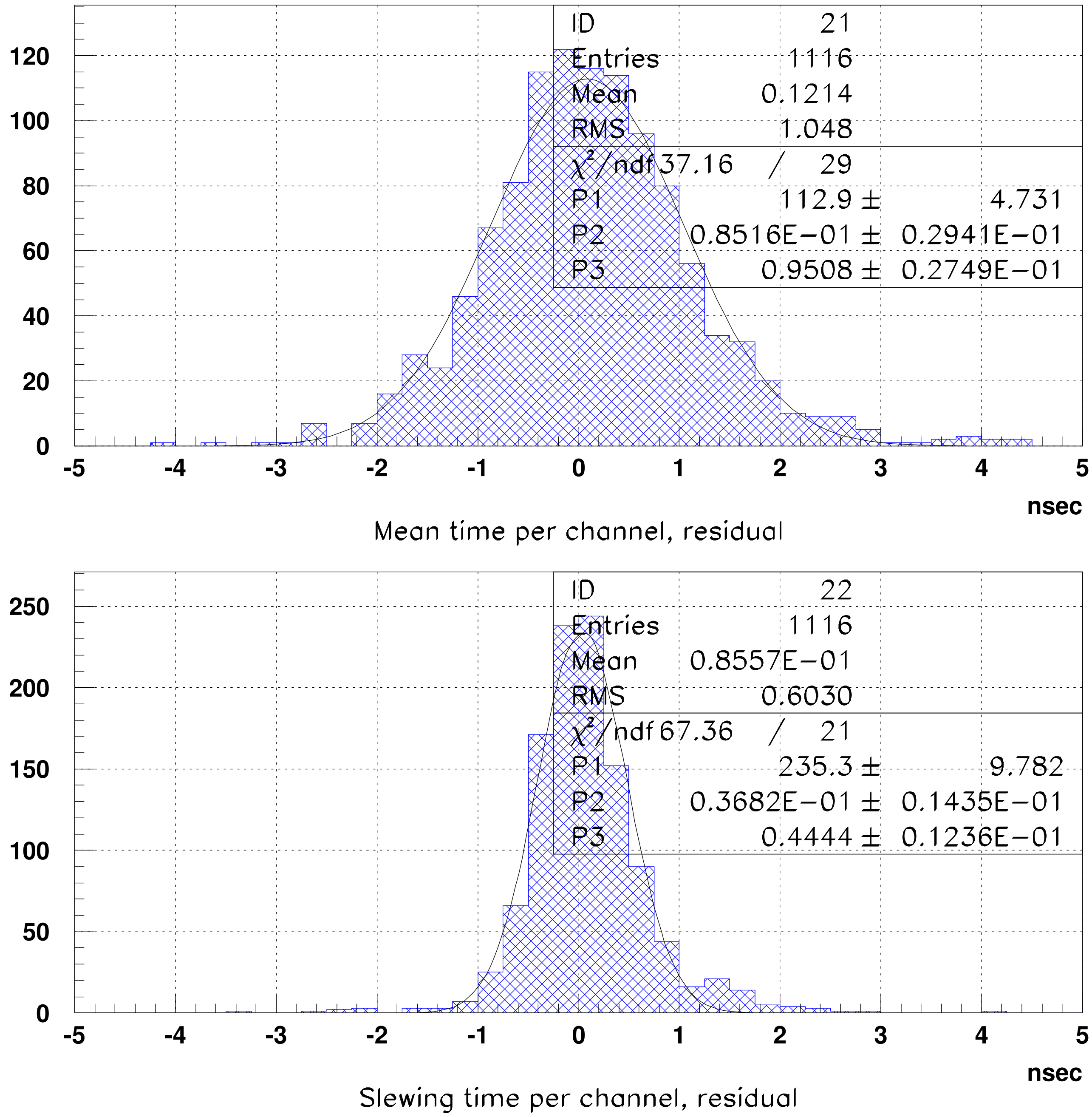


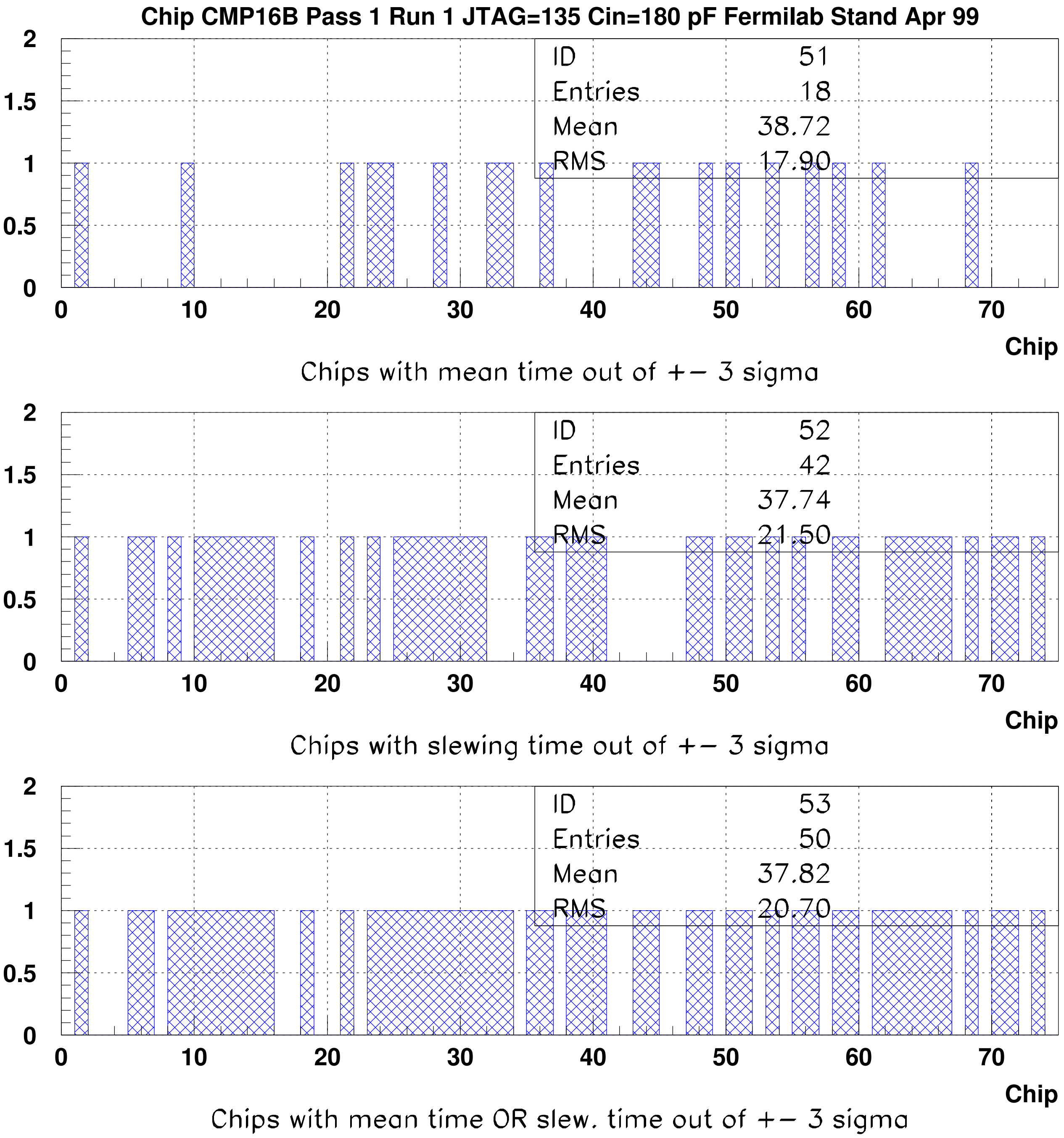
Aver/chan. Mean time vs chip



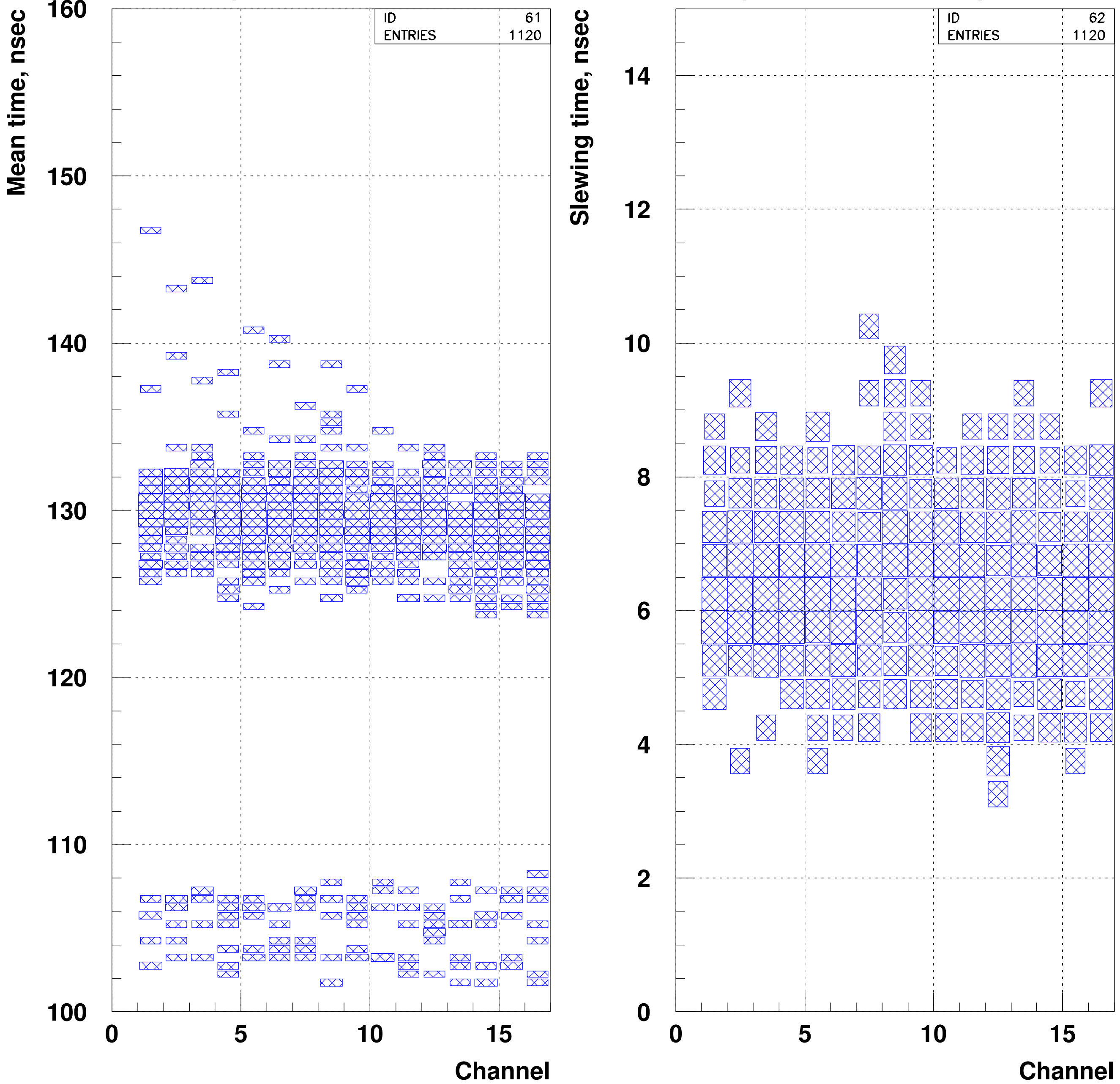
Aver/chan. Slewing time vs chip

Chip CMP16B Pass 1 Run 1 JTAG=135 Cin=180 pF Fermilab Stand Apr 99




Chip CMP16B Pass 1 Run 1 JTAG=135 Cin=180 pF Fermilab Stand Apr 99
ID 51
Entries 18
Mean 38.72
RMS 17.90
Chip
Chips with mean time out of +− 3 sigma
ID 52
Entries 42
Mean 37.74
RMS 21.50
Chip
Chips with slewing time out of +− 3 sigma
ID 53
Entries 50
Mean 37.82
RMS 20.70
Chip
Chips with mean time OR slew. time out of +− 3 sigma

Chip CMP16B Pass 1 Run 1 JTAG=135 Cin=180 pF Fermilab Stand Apr 99



Mean time vs channel number, all chips   Slewing time vs channel number, all chips